

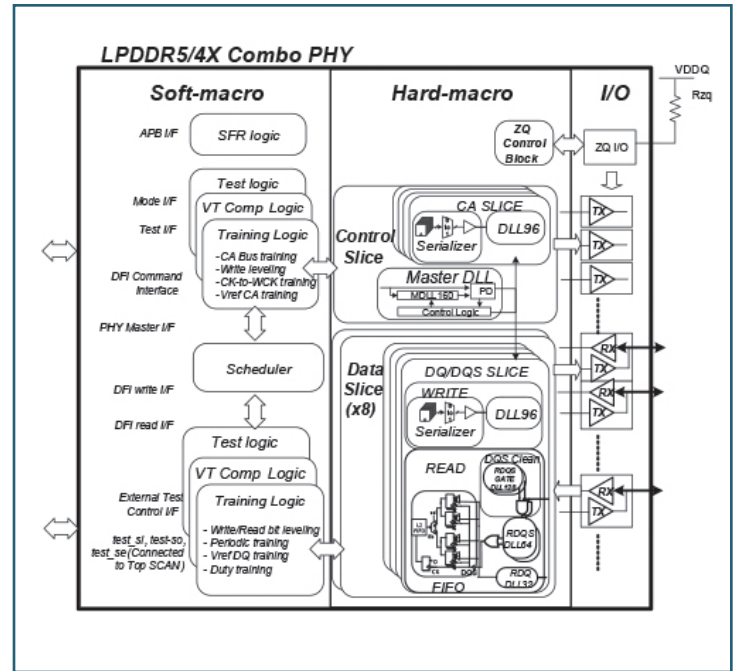
LPDDR5/4X PHY

Overview

This PHY can support 5.5Gbps LPDDR5 / 4.266Gbps LPDDR4 with a 16-bit 1-channel configuration. It is DFI4.0 compliant with frequency ratios of 1:4 and 1:2.

Key Features

- 8nm low power CMOS device technology
- Supports 4.266Gbps LPDDR4
- Supports 5.5Gbps LPDDR5
- Off-chip driver (OCD)
- Support Retention IO
- On-die term (VSSQ-TERM)
- Auto training after memory initialization
 - o Command bus training
 - o Auto-DQS cleaning
 - o Write/Read bit training
 - o Vref CA/DQ training
- Power supply
 - o 0.75V PHY core voltage (Typ.)
 - o 0.6V for LPDDR4 IO, 0.5V for LPDDR5 IO



Deliverables

- Model and softmacro netlist
- Timing DK
- DFT model and netlist
- CIR, GDS(GDS2), and LEF
- Datasheet

For more information, please contact us at IP@silvaco.com.

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